

APPROVED	O.G. FIG.
BY	CLASS SUBCLASS
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OTO et al.; Appln. No. 09/817,233; Conf. No.:
Manufacturing Method of Semiconductor Device Having DRAM Capacitors"

1/8

FIG. 1
RELATED ART

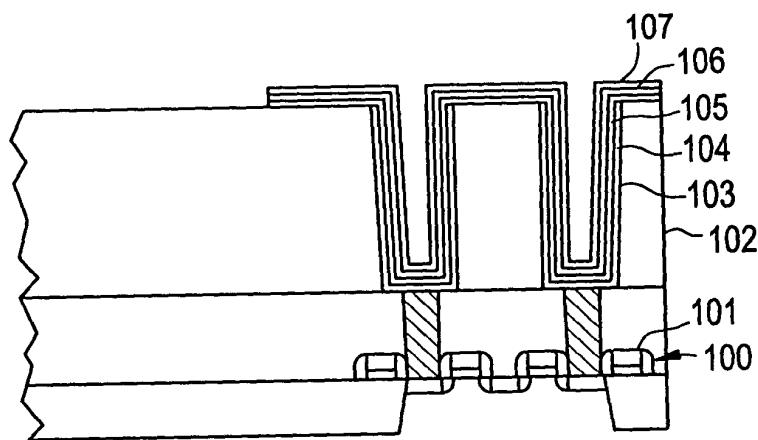
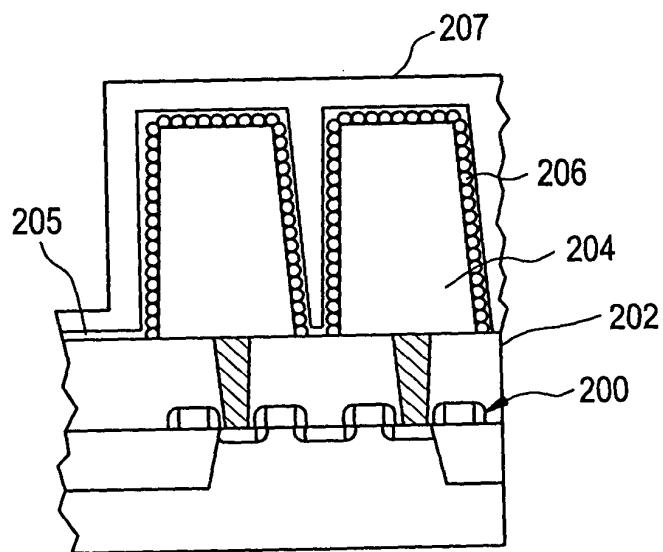


FIG. 2
RELATED ART



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2/8

FIG. 3A
RELATED ART

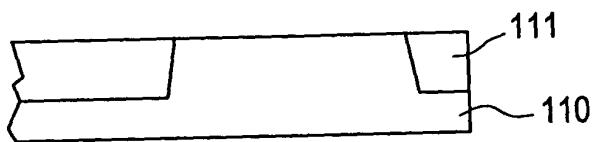


FIG. 3B
RELATED ART

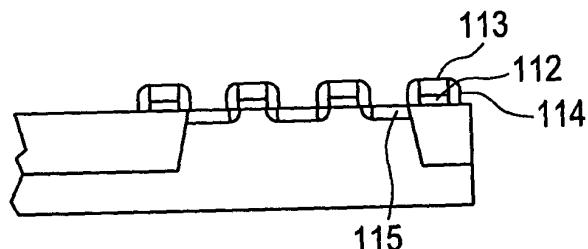


FIG. 3C
RELATED ART

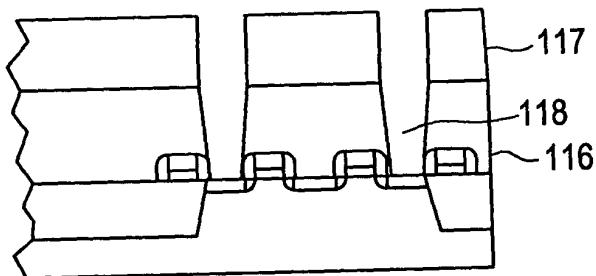
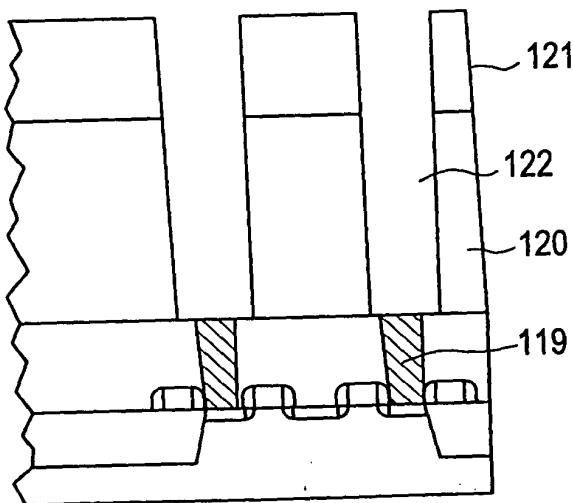


FIG. 3D
RELATED ART



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3/8

FIG. 3E
RELATED ART

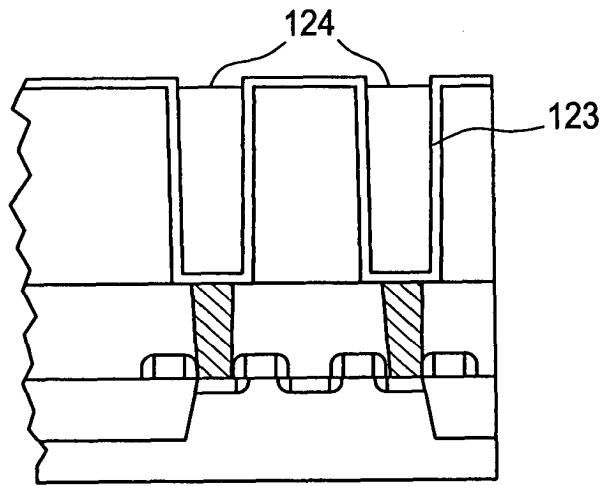
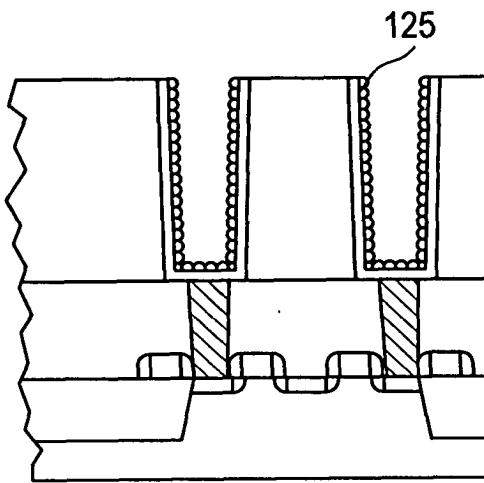


FIG. 3F
RELATED ART



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4/8

FIG. 3G
RELATED ART

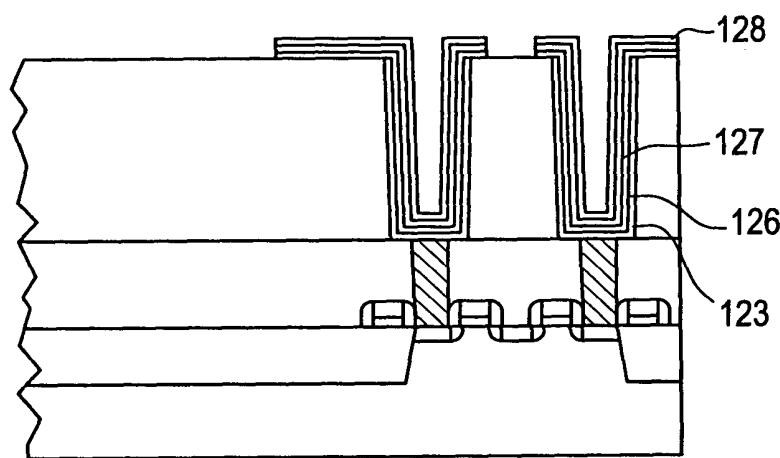
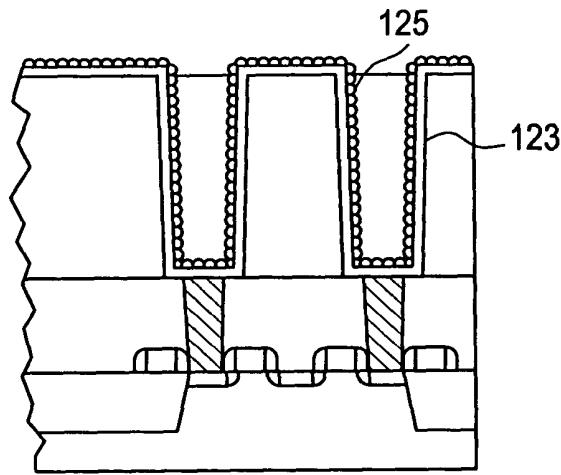


FIG. 4

PRIOR ART



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5/8

FIG. 5A
RELATED ART

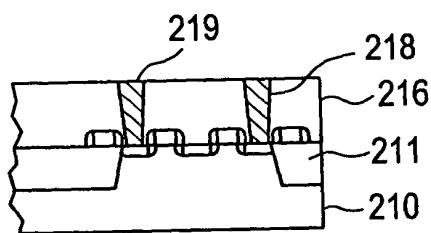


FIG. 5B
RELATED ART

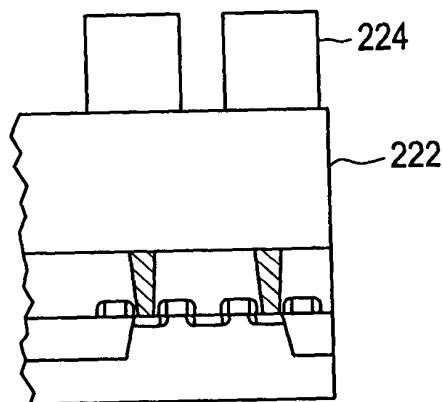


FIG. 5C
RELATED ART

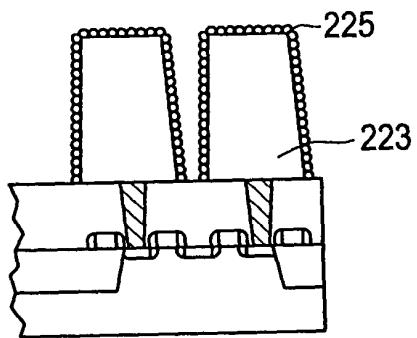
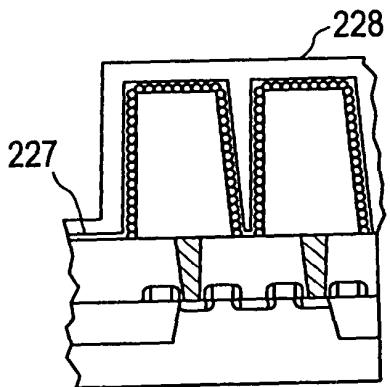


FIG. 5D
RELATED ART



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6/8

FIG. 6A
RELATED ART

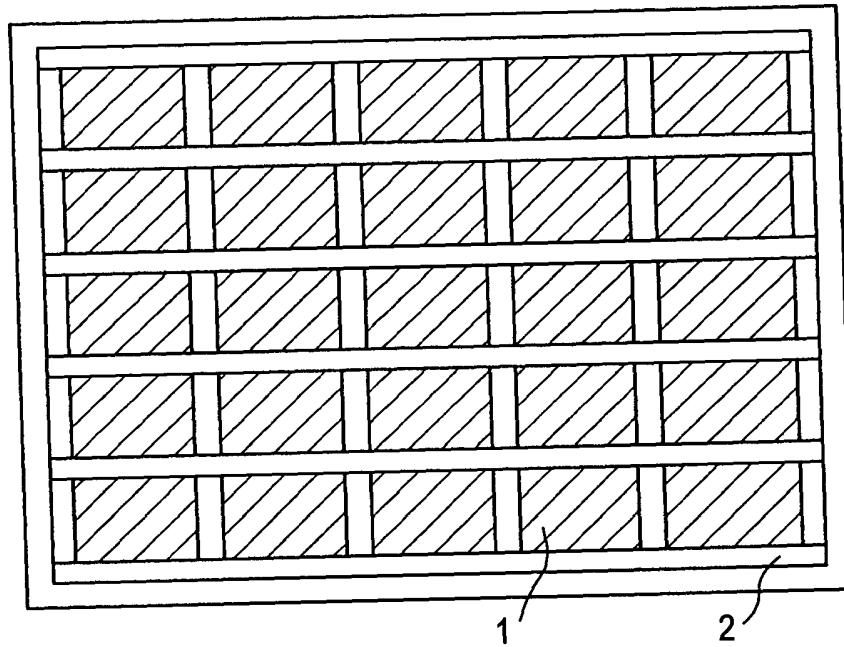
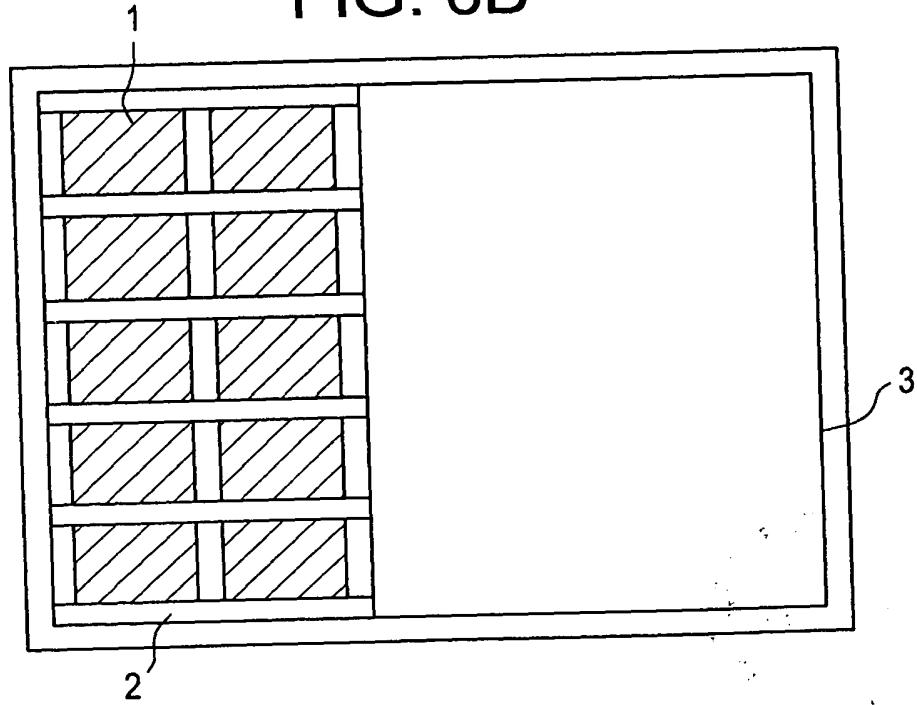


FIG. 6B



7/8

FIG. 7A

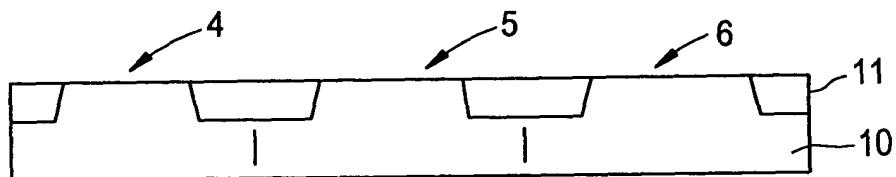


FIG. 7B

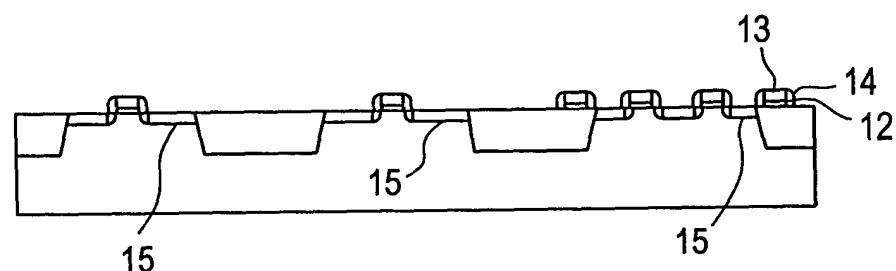


FIG. 7C

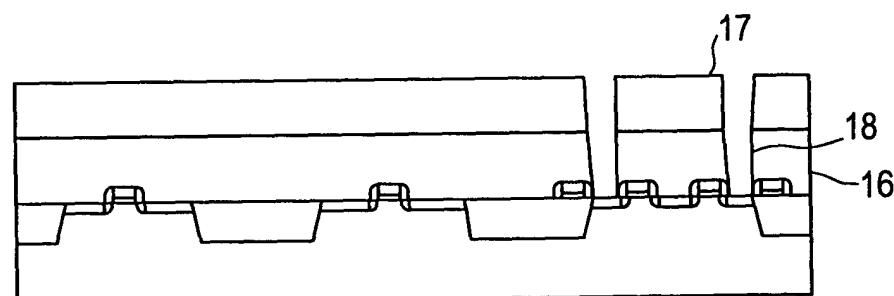
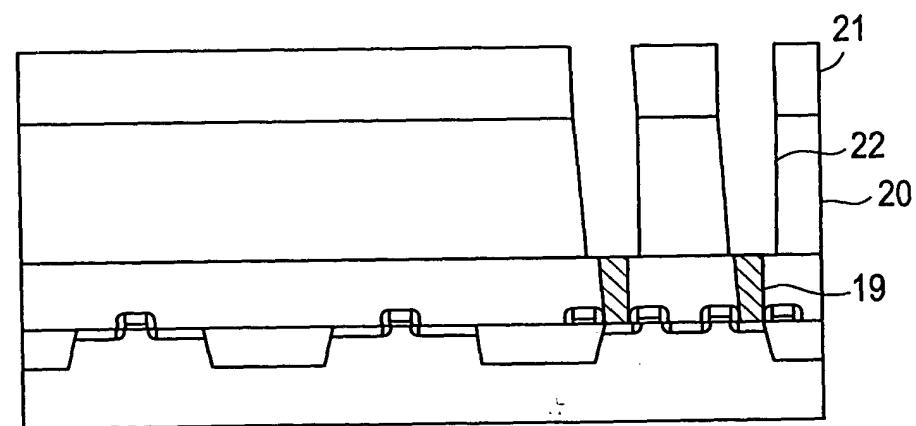


FIG. 7D



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8/8

FIG. 7E

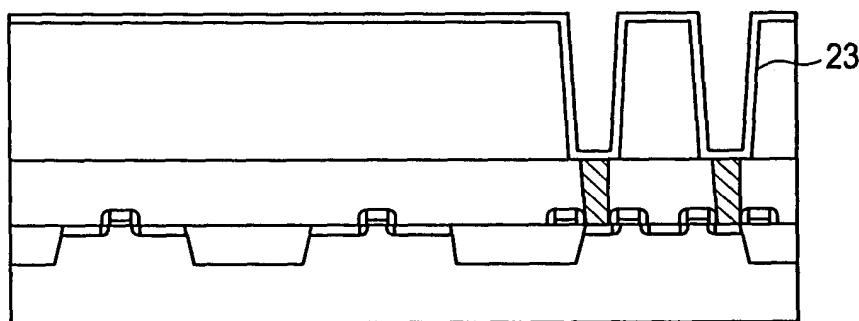


FIG. 7F

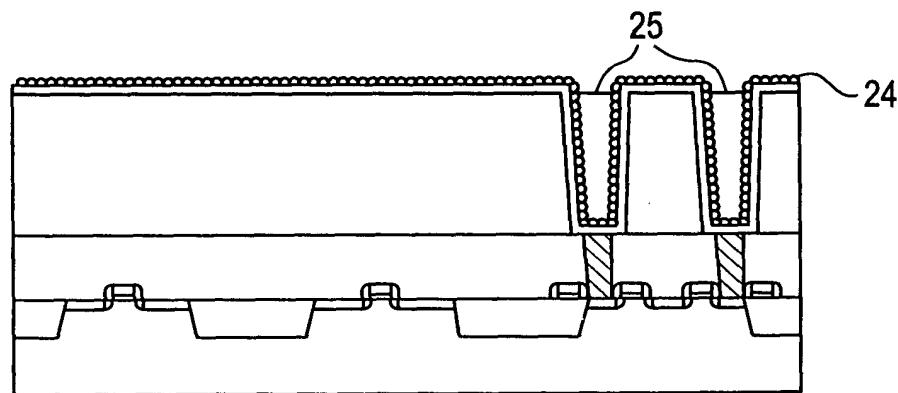
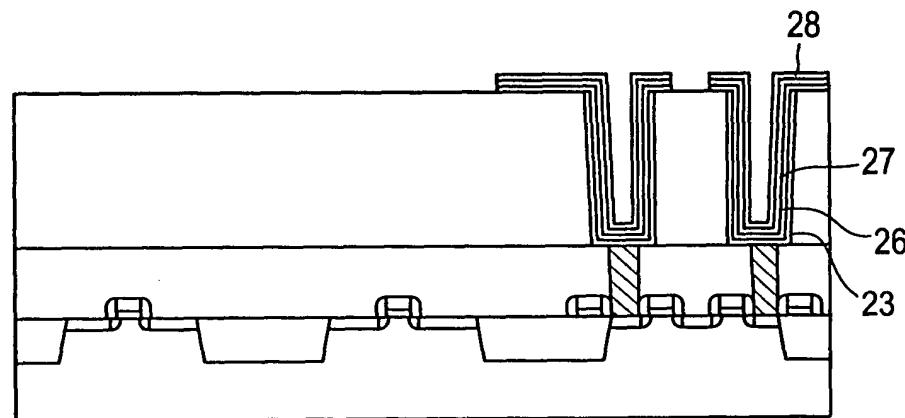


FIG. 7G



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